

Offset	Field	Definition	Description
23h	8	WPS PBC	WPS Push Button Configuration control. GPIO [0] pin-113 will be used as PBC status, this GPIO pin will be treated as active low. For LED mode#2 (Signal strength indication mode) will not support PBC, because this LED mode also used this GPIO pin.
			0
	1	ON: Enable WPS PBC function,	
15:9	Reserved	Reserved	Reserved

**Example:**

Setting	Value of bit [3:2:1:0]
TX/RX diversity for 4 antennas	0:0:1:1
TX/RX diversity for 2 antennas; Antenna use E2/E3	0:1:1:0
Fixed 2 antenna TX on E4; RX on E2/E4	1:1:0:0

**1.3 Country Region Code for 5G band (24h)**

Default value = FFh, which means read from INF and registry, more flexible than reading from EEPROM, this is our current InstallShield CCS implementation. **We do not recommend customers to read SKU from EEPROM.** The FFh is default value.

CountryCode— Specify the domain code, can be FFh or one of the followings,

Value	Support Channels
0	36, 40, 44, 48, 52, 56, 60, 64, 149, 153, 157, 161, 165
1	36, 40, 44, 48, 52, 56, 60, 64, 100, 104, 108, 112, 116, 120, 124, 128, 132, 136, 140
2	36, 40, 44, 48, 52, 56, 60, 64
3	52, 56, 60, 64, 149, 153, 157, 161
4	149, 153, 157, 161, 165
5	149, 153, 157, 161
6	36, 40, 44, 48
7	36, 40, 44, 48, 52, 56, 60, 64, 100, 104, 108, 112, 116, 120, 124, 128, 132, 136, 140, 149, 153, 157, 161, 165
8	52, 56, 60, 64
9	34, 38, 42, 46
10	34, 36, 38, 40, 42, 44, 46, 48, 52, 56, 60, 64

#### 1.4 Country Region Code for 2.4G band (25h)

Default value = FFh, which means read from INF and registry, more flexible than reading from EEPROM, this is our current InstallShield CCS implementation. **We do not recommend customers to read SKU from EEPROM.** Value FFh is our default value.

CountryCode— Specify the domain code, can be FFh or one of the followings,

Value	Support Channels
0	1 – 11
1	1 – 13
2	10 – 11
3	10 – 13
4	14
5	1 – 14
6	3 – 9
7	5 – 13

**Notes:** If set to read SKU from EEPROM, only available if both 5G and 2.4G Country Region code registers are programmed.

#### 1.5 BBP Instructions (total 16 instructions) (26h ~45h)

16 spare BBP instructions are reserved in E2PROM; each instruction is a <BBP register ID, BBP register value> pair which instructs device driver to initialize the specified BBP register with the specified value upon NIC initialization.

BBP instructions with value <FF, FF> or <00,00> are considered invalid and will be ignored.

#### 1.6 2.4G band TX Power (46h ~53h)

To prevent reading from EMPTY E2PROM, driver treats these “Channel xx Tx Power” value 0 and any value  $\geq 32$  as invalid. That is, only bit [0..4] in each byte contains valid data, [bit 5..7] MUST be 0. **This TX power level is configured into RF R3 TX[0..4] bits. If a TX power value is invalid, driver will use hard-coded value 24 for this channel.**

#### 1.7 5G band TX Power (62h ~79h, 7Dh ~80h)

To prevent reading from EMPTY E2PROM, driver treats these “Channel xx Tx Power” value 0 and any value  $\geq 32$  as invalid. That is, only bit[0..4] in each byte contains valid data, bit [5..7] MUST be 0.

#### 1.8 Tx Power delta TSSI Boundary (54h ~ 5ah, 90h ~ 98h)

Driver compares current TSSI value (from BBP R1) with this TSSI reference value as a base to decide if real-time TX power compensation is required. 0xFF will be treated as invalid value. This function is controlled by ‘TX AGC’ bit in NIC configuration byte.